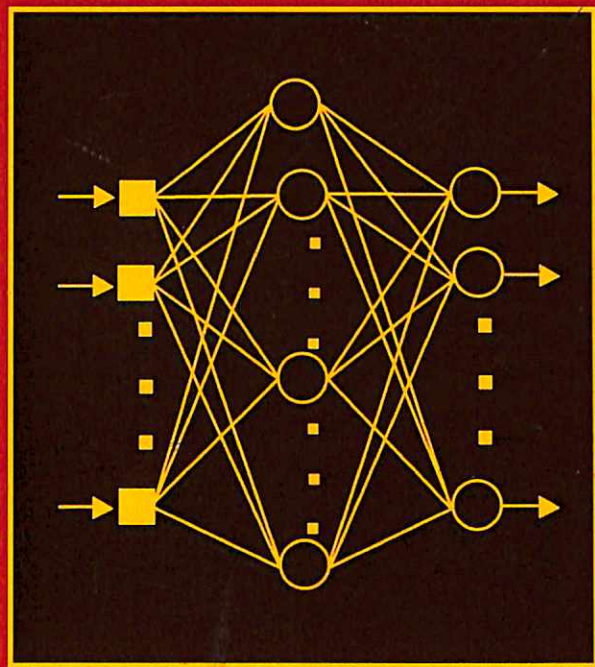


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*Dark Silicon and  
Future On-chip Systems*



*Edited by*

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# CONTENTS

<i>Preface</i>	<i>ix</i>
<b>1. Dark Silicon and the History of Computing</b>	<b>1</b>
Pejman Lotfi-Kamran and Hamid Sarbazi-Azad	
1. Introduction and Background	2
2. The Single-Core Era	3
3. The Multicore Era	7
4. The Dark Silicon Era	11
5. Conclusion	21
References	21
About the Authors	32
<b>2. Revisiting Processor Allocation and Application Mapping in Future CMPs in Dark Silicon Era</b>	<b>35</b>
Mohaddeseh Hoveida, Fatemeh Aghaaliakbari, Majid Jalili, Ramin Bashizade, Mohammad Arjomand, and Hamid Sarbazi-Azad	
1. Introduction	36
2. Related Work	38
3. SCMesh: A Scalable and High Bandwidth NoC	42
4. Strategy 1: Revisiting Processor Allocation	46
5. Strategy 2: Revisiting Application Mapping	53
6. Evaluation	64
7. Conclusions	75
References	76
About the Authors	78
<b>3. Multiobjectivism in Dark Silicon Age</b>	<b>83</b>
Amin Rezaei, Masoud Daneshtalab, and Hai Zhou	
1. Introduction and Background	84
2. Shift Sprinting: Reliable Temperature-Aware NoC-Based MCSoc Architecture in Dark Silicon Age	87
3. Round Rotary Mapping: Temperature- and Congestion-Aware Application Mapping Approach for Wireless NoC in Dark Silicon Age	104
4. Conclusion and the Future Outlook	122
References	123
About the Authors	125



<b>4. Dark Silicon Aware Resource Management for Many-Core Systems</b>	<b>127</b>
Heba Khdr, Santiago Pagani, Muhammad Shafique, and Jörg Henkel	
1. Introduction	128
2. State-of-the-Art Resource Management Techniques	134
3. System Model	136
4. Problem Definition	140
5. Dark Silicon Aware Resource Management	141
6. Experimental Evaluations	153
7. Dark Silicon Aware Resource Management for Heterogeneous Many-Core Systems	164
8. Conclusions	165
Acknowledgments	165
References	165
About the Authors	168
<b>5. Dynamic Power Management for Dark Silicon Multicore Processors</b>	<b>171</b>
Siddharth Garg	
1. Introduction	172
2. Dark Silicon Aware Microarchitectural Adaptation for Homogeneous Multicores	173
3. Dynamic Scheduling for Asymmetric Multicores	186
4. Dynamic DoP and Cluster Migration on Asymmetric Multicores	200
5. Empirical Analysis	205
6. Conclusion	211
References	211
About the Author	216
<b>6. Topology Specialization for Networks-on-Chip in the Dark Silicon Era</b>	<b>217</b>
Mehdi Modarressi and Hamid Sarbazi-Azad	
1. Introduction	218
2. Dark Silicon	220
3. Core Specialization	225
4. Specialized NoC for Specialized Cores	228
5. Low-Latency and Power-Efficient NoC Architectures	232
6. Architecture Support for Topology Reconfiguration	236
7. Topology Reconfiguration Procedure	241

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8. Evaluation	248
9. Conclusion	251
References	252
About the Authors	257
<b>7. Introduction to Emerging SRAM-Based FPGA Architectures in Dark Silicon Era</b>	<b>259</b>
Zeinab Seifoori, Zahra Ebrahimi, Behnam Khaleghi, and Hossein Asadi	
1. Introduction	260
2. Architecture of SRAM-Based FPGAs	261
3. Power Wall and Dark Silicon	266
4. Logic Block Architectures in Dark Silicon Era	270
5. Routing Block Architectures in Dark Silicon Era	279
6. Discussion and Conclusion	289
References	289
About the Authors	292